

# SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

# **2SK4198FS** — General-Purpose Switching Device Applications

#### **Features**

- ON-resistance RDS(on)= $1.8\Omega$  (typ.)
- 10V drive

- Input capacitance Ciss=360pF (typ.)
- · Repetitive avalanche guarantee

#### **Specifications**

Absolute Maximum Ratings at Ta=25°C

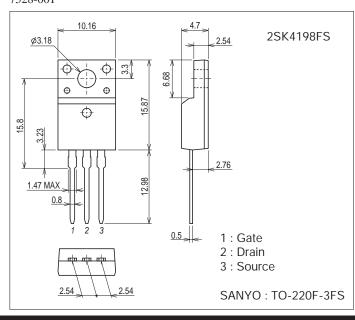
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		600	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±30	V
D. (1.0)	I <sub>Dc</sub> *1	Limited only by maximum temperature Tch=150°C	5	Α
Drain Current (DC)	I <sub>Dpack</sub> *2	Tc=25°C (SANYO's ideal heat dissipation condition)*3	4	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	18	А
Allowable Power Dissipation	D-		2.0	W
	PD	Tc=25°C (SANYO's ideal heat dissipation condition)*3	30	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *4	EAS		55	mJ
Avalanche Current *5	IAV		4.5	А
Avalanche Energy (Repetition)	EAR	Limited only by maximum temperature Tch=150°C	3	mJ

Note: \*1 Shows chip capability.

- \*2 Package limited.
- \*3 SANYO's condition is radiation from backside.
- The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.
- \*4 V<sub>DD</sub>=50V, L=5mH, I<sub>AV</sub>=4.5A (Fig.1)
- \*5 L≤5mH, Single pulse

#### **Package Dimensions**

unit : mm (typ) 7528-001



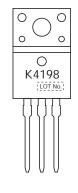
#### **Product & Package Information**

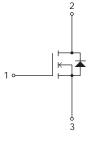
• Package : TO-220F-3FS

• JEITA, JEDEC : SC-67

• Minimum Packing Quantity : 50 pcs./magazine

#### Marking Electrical Connection





#### **SANYO Semiconductor Co., Ltd.**

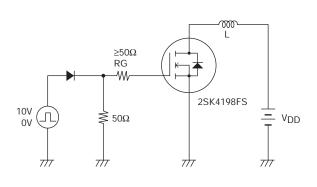
http://www.sanyosemi.com/en/network/

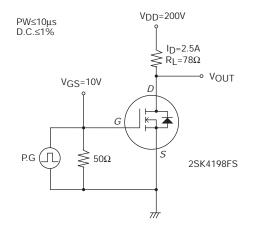
#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit	
Parameter	Syllibol	Conditions	min	typ	max	Unit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	600			V	
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =480V, V <sub>GS</sub> =0V			100	μΑ	
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V			±100	nA	
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	3		5	V	
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =2.5A	1.2	2.4		S	
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =2.5A, V <sub>G</sub> S=10V		1.8	2.34	Ω	
Input Capacitance	Ciss			360		pF	
Output Capacitance	Coss	V <sub>DS</sub> =30V, f=1MHz		69		pF	
Reverse Transfer Capacitance	Crss			15		рF	
Turn-ON Delay Time	t <sub>d</sub> (on)			13		ns	
Rise Time	t <sub>r</sub>	Son Fig 2		28		ns	
Turn-OFF Delay Time	t <sub>d</sub> (off)	See Fig.2		39		ns	
Fall Time	tf			15		ns	
Total Gate Charge	Qg			14.3		nC	
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A		3.0		nC	
Gate-to-Drain "Miller" Charge	Qgd			8.2		nC	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =5A, V <sub>GS</sub> =0V		0.9	1.2	V	

Fig.1 Unclamped Inductive Switching Test Circuit

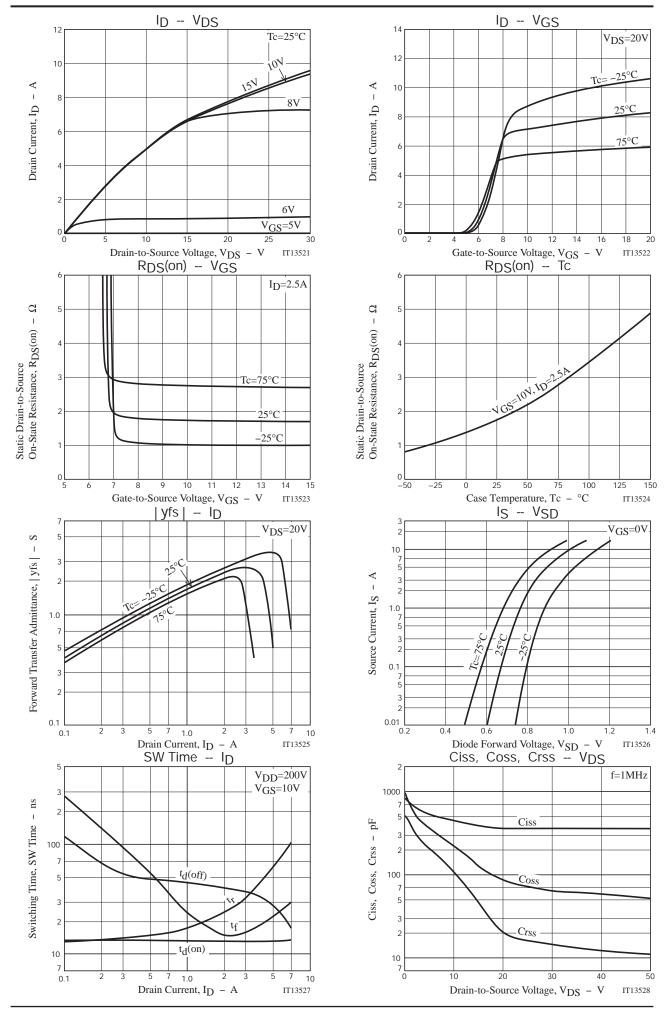
Fig.2 Switching Time Test Circuit

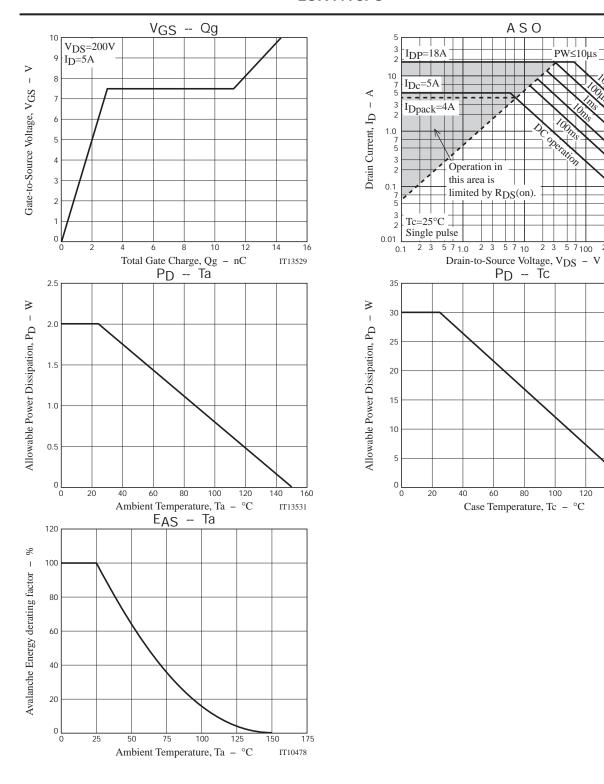




### **Ordering Information**

Device	Package	Shipping	memo
2SK4198FS	TO-220F-3FS	50pcs./magazine	Pb Free





IT14229

140

160

IT13532

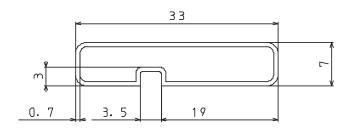
#### Magazine Specification

2SK4198FS

#### 1. Packing Format

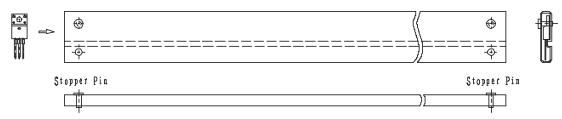
Package Name	Magazine Name	Maximum Jame devices c			Packing format		
1 4 4 4 6 4 1 (4 4 4 4	Idag as the Hams	I	Inner box	Outer box	Inner BOX	Outer BOX	
TO-220F-3F\$	TO-220F	50	1, 000	4,000	SPD-0V0001 20 magazines contained Dimensions:mm (external) 568×150×55	SPT-081029 4 inner boxes contained Dimensions:mm (external) 590×225×178	

#### 



Tolerance=±0, 3mm
Thickness=0, 7±0, 2mm
Length =532, 5±2mm
Material =PVC (Antistatic treatment)

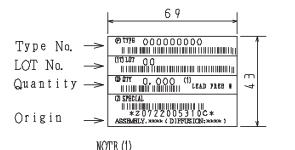
#### 3. Storage method to magazine

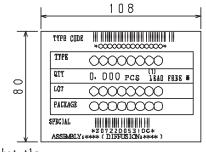


4. Inner box label (unit:mm)



It is a label at the time of factory shigments. The form of a label may change in physical distribution process.



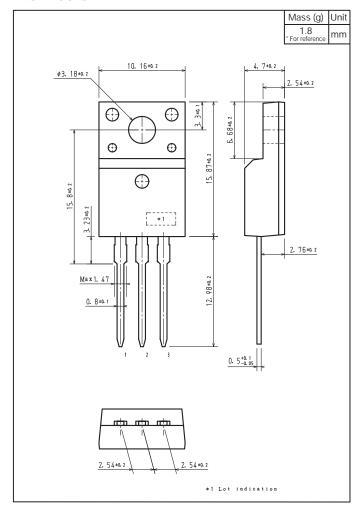


The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

Label		JEITA Phase			
LEAD FREE	3	JEITA Phase 3A			

## **Outline Drawing**

2SK4198FS



Note on usage: Since the 2SK4198FS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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